

SUCCESSLCD AUTHORIZED TECHNICAL REPRODUCTION

COM31H3P76ULC - 50-PIN PARALLEL RGB ASSIGNMENT

Source specification 22TLM118 | Version 0.0 | Issue 2023-02-15 | source page 12/29

TENTATIVE / UNDER DEVELOPMENT / SUBJECT TO CHANGE

Pin	Symbol	Function
1	GND	Ground
2	BL_A	LED drive power source - anode
3	BL_C	LED drive power source - cathode
4	BL_C	LED drive power source - cathode
5	IOVDD	Power supply for I/O - 3.3 V nominal
6	IOVDD	Power supply for I/O - 3.3 V nominal
7	VDD	Power supply for circuit - 3.3 V nominal
8	VDD	Power supply for circuit - 3.3 V nominal
9	R7	Red data input [MSB]
10	R6	Red data input
11	R5	Red data input
12	R4	Red data input
13	R3	Red data input
14	R2	Red data input
15	R1	Red data input
16	R0	Red data input [LSB]
17	G7	Green data input [MSB]
18	G6	Green data input
19	G5	Green data input
20	G4	Green data input
21	G3	Green data input
22	G2	Green data input
23	G1	Green data input
24	G0	Green data input [LSB]
25	B7	Blue data input [MSB]
26	B6	Blue data input
27	B5	Blue data input
28	B4	Blue data input
29	B3	Blue data input
30	B2	Blue data input
31	B1	Blue data input
32	B0	Blue data input [LSB]
33	GND	Ground
34	HSYNC	Horizontal synchronization - negative polarity
35	NC	Open
36	VSYNC	Vertical synchronization - negative polarity
37	GND	Ground
38	DE	Data enable - positive polarity
39	GND	Ground
40	CLK	Clock signal - data latched at falling edge
41	GND	Ground
42	IOVDD	Power supply for I/O - 3.3 V nominal
43	GND	Ground
44	GND	Ground
45	GND	Ground
46	STBYB	Standby input - High: normal; Low: standby
47	NC	Open
48	RESETB	Reset input - Low-active
49	NC	Open
50	GND	Ground

DE mode note: The source marks HSYNC and VSYNC with a note: in DE mode they may be fixed High or Low.

Engineering control: Confirm final timing, mating connector orientation and controlled revision before PCB release.